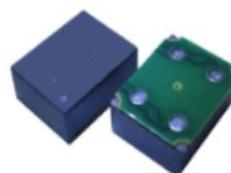


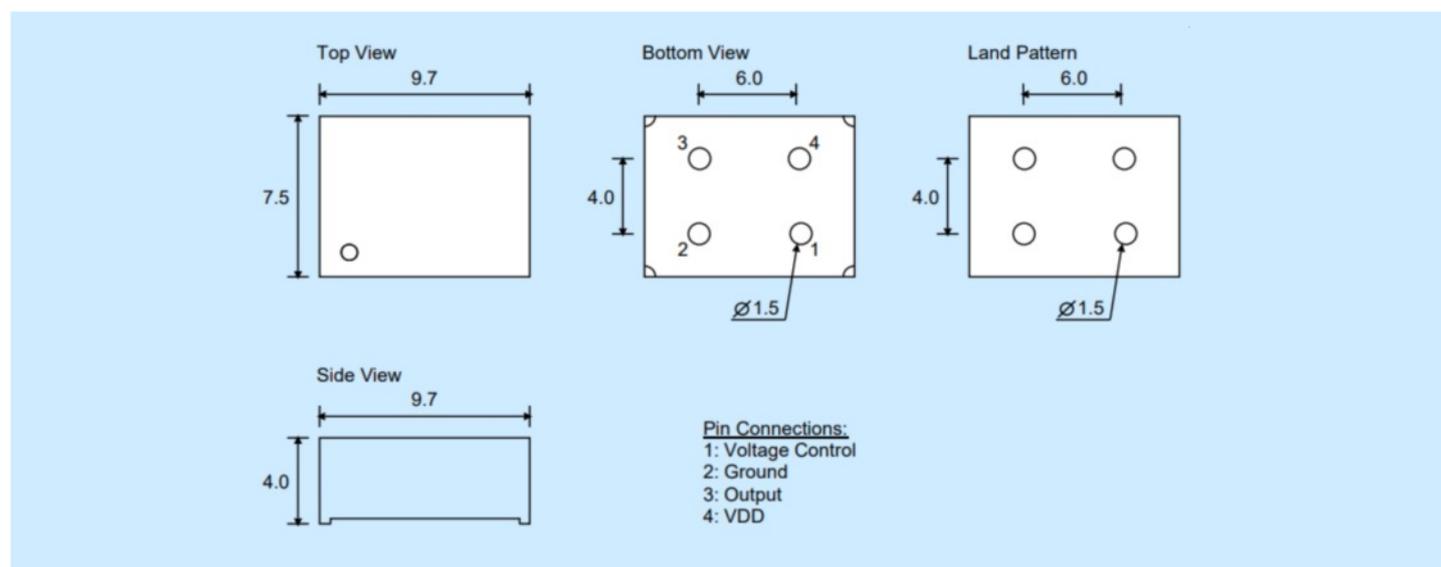
- 9.7 x 7.5 x 4.0mm package
- Surface mount package
- +3.3V, +5.0V supply voltage options
- Clipped sine and square wave output
- Electronic Frequency Tuning as standard



### GENERAL SPECIFICATION

Output Waveform		Square Wave		Clipped Sine Wave	
		+3.3V±5%	+5.0V±5%	+3.3V±5%	+5.0V±5%
Supply Voltage		+3.3V±5%		+5.0V±5%	
Frequency Range		5.0 ~ 40.0MHz		5.0 ~ 40.0MHz	
Initial Calibration Tolerance		±500ppb (max.)		±500ppb (max.)	
Crystal Cut		Vcon = +1.65V	Vcon = +2.5V	Vcon = +1.65V	Vcon = +2.5V
		IT-cut			
Frequency Stability	vs Temperature	±10ppb max. over -30°C to +70°C			
		±20ppb max. over -40°C to 85°C			
	vs Voltage Change	±10ppb max. for ±5% voltage change			
	vs Warm-up Time (+25°C)	5 min. max., within ±0.1ppm of its reference frequency			
	vs Aging	±3ppb max. after 30 days, ±600ppb max. first year, ±3ppm max. over 10 years			
Voltage Control (EFC)	Frequency Deviation Range	> ±5ppm reference to Fo at +25°C and over temp. range			
	Control Voltage Range	+1.65±1.65V		+2.5±2.5V	
	Transfer Function	Positive: Increasing control voltage increases output frequency			
	Input Impedance	100k Ω min.			
	EFC Linearity	±10% max.			
Power Dissipation (at +25°C)		0.4W max. at steady state; 350mA max. at turn-on			
Output	Load	15pF		10kΩ // 10pF±10%	
	Output Logic High	+2.4V min.	+4.5V min.	---	
	Output Logic Low	+0.4V max.		---	
	Duty Cycle	50±5% at +1.65V		---	
	Rise and Fall Time	7nsec. max (20% ~ 80% of waveform)		---	
	Output Voltage Level (p-p)	---		0.8V min.	
	Phase Noise Offset (typ. at 10.0MHz)	10Hz	100Hz	1kHz	10kHz
	-98dBc	-126dBc	-145dBc	-152dBc	

### PACKAGE OUTLINE



**ORDERING/PART NUMBER GENERATION**Example: EOC51T3 - 25.000MHz - 500/0+70Series Designation  
EOC51Output Waveform  
T = CMOS  
S = Sine WaveSupply Voltage  
3.3V = 3  
5.0V = 5

Frequency

Frequency Stability

Operating Temperature Range